

# Analog Sensor Jitter Analysis in Power System Control

RAMESH K. RAYUDU

Institute of Information Sciences and Technology,  
Massey University, Palmerston North, New Zealand  
R.K.Rayudu@massey.ac.nz

## Abstract

This paper presents an algorithm for jitter analysis of analog sensors in power system control. The objective is to identify the problem analog sensors and establish the fault for each analog signal jitter. The solution is to design a knowledge-based algorithm that includes a knowledge base to identify the problem analogs and the equipment responsible for the jitter. A blackboard architecture is developed for real-time analysis. The approach is validated by experimental results carried on the actual power system.

**Keywords:** Analog Jitters, Fault Analysis, Power Systems, Analog Measurements, Knowledge Engineering.

## 1 Introduction

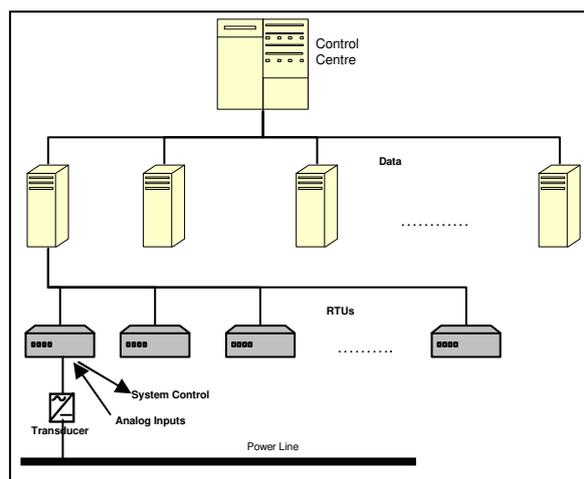
New Zealand's power system is controlled by a two-tier control centres; a main control centre and three area operation centres. The control centres are equipped with a SCADA (Supervisory Control and Data Acquisition) system which provides the operators with the low level validation of the data by communicating with data concentrators which control several Remote Terminal Units (RTUs) (Figure 1). Analogue, Control, and Status indication values from the power plants, switch yards, and substations are fed into RTUs which are sent to SCADA through data concentrators (Figure 1). Data concentrators manage the data sent by RTUs before sending it to the control centre. Research described here is related only to the analog values and not to the control and status values which indicate the status of the equipment. Analogue values are a measure of voltage or electrical current such as Power (MW), Voltage (V), Power Reactance (MV), and Current (A) are converted to digital signals by a transducer before sending to SCADA via Input/Output cards in the RTUs.

Whenever there is a fault, the analogues become unstable and read wrong values. This instability in analogue values is called "jitters". The jitters in analogues can be caused by several factors such as load fluctuations, faulty hardware and/or software in any part of the control equipment. If the jitter is due to load fluctuations, the analogue needs no analysis; otherwise, an analysis is needed.

Since the jitter analogues are not important when compared to other power system faults, such as system breakdown and power failure, they are always

regarded as secondary problems. However, these analogues have some influence on the real-time power system control and cannot be neglected. The analogs provide real-time measurements of network quantities such as voltage and current and analog jitter leads to spurious values. Analog jitters are one of reasons for real-time state estimation [2, 6, 10, 12]. State estimation estimates the analog value through several algorithms and determines if the original analog value is spurious. But the analog that exhibits jitter is not addressed and will be the responsibility of the hardware personnel to deal with it.

There is a need for a system that can identify a jitter sensor and notify the relevant contractor so that the analog can be repaired [2].



**Figure 1:** Network connections between control centre and power lines.

Jitter in analogs can be caused by genuine reasons (fluctuating measurements) or by problematic reasons. To analyse the jittery analogues, an expert having

relevant good experience and technical knowledge, is required.

The approach described in this paper attempts to meet the industry's needs by analysing the jitter reporting analogs, finding the 'genuinely' fault-related analogs and diagnosing them for the cause of jitter, and notifying the concerned technician about the problem. The characterisation of a knowledge based system for the above tasks in terms of knowledge, data and control of processing incorporated in the system is presented followed by a case study.

## 2 Architecture of Analog Jitter Analysis (AJA)

The implementation of the expert system is on a P4 3200 machine using VisualProlog V 5.2. The architecture is based on blackboard architecture [8,9]. It communicates with SCADA for accessing the necessary data required for the analysis and the diagnosis. The data thus acquired from SCADA are stored in 5 different Oracle databases and used at different instances of the problem solving process. The main problem solving knowledge of the domain expert is represented in rules which is termed "expert knowledge base" in this system. The general technical knowledge of the expert has been separated and included in another knowledge base called "general knowledge". Apart from these knowledge bases, the system incorporates another knowledge base termed "strategic common sense" which includes the common sense knowledge of the domain expert. The diagnostic algorithm loops through the rules of the expert knowledge base, the general knowledge base, and uses the strategic common sense to analyse and diagnose the faults with the help of the data (expert database) available from SCADA. Figure 2 shows the general architecture of this expert system.

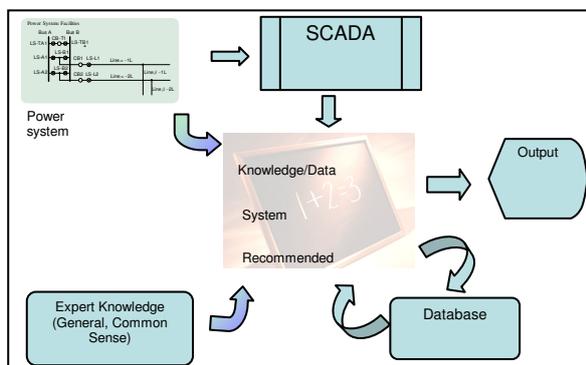


Figure 2: System Architecture.

## 3 Expert Knowledge and Data

The knowledge included in this system is obtained from the control centre's sole expert in fault diagnosis. The process of the analysis starts with the identification of the analogs with jitter count which is a value indicating the number of times the analogue has been jittery within a specified length of time. The maximum jitter count is called the "maximum jitter record" and all the analogues which have the jitter count above 10% of this "maximum jitter record" are considered for further analysis. This preliminary exclusion eliminates the unimportant analogues from the 2080 analogues in the power system. The selected analogues are then divided into 5 groups: lake, rotary condenser, line, generator and transformer analogues. Lake and rotary condenser analogues are eliminated as their jitter is mainly due to genuine changes such as lake level changes and load fluctuations. The remaining analog groups (line, transformer, and generator) are further analysed by using procedures specific to each group. In general, when a jittery analogue is analysed, a check is made whether any other analogues from the same station or same RTU, are jittery. If they are jittery, then further analysis is carried by regarding all these analogues as a set. If this set not jittery, then the analogue is analysed as a single analogue. The analogue is then checked for its properties such as scanning priority<sup>1</sup> and fixed scale deflection<sup>2</sup> to find whether the analogue is operating under the normal conditions.

Once the analogue's normality is established, the analysis moves to identify any parallel or series circuit analogs associated with the problematic analog. Then, the analog values of these analogs are compared. Depending on this comparison, further analysis is made which is highly heuristic in nature and involves several decision loops. For example, if the circuit containing the problem analogue has a parallel circuit, then the analog value is compared with that of the parallel circuit. If both values are similar, the process moves to the identification of several factors associated with the second analogue. During the process, attempts are made to establish the relationship between the two analogues with respect to the jitter and the problem that caused the jitter. During these attempts, the decision path leads to decision loops, and further the decision making process moves through the loops, closer it gets to the establishment of the fault. The output of this analysis suggests whether the jitter is fault-related, and also

<sup>1</sup> Scanning priority is the priority assigned to every analogue in the power system.

<sup>2</sup> Fixed scale deflection is the maximum deflection rate assigned to each analogue. Whenever the analogue exceeds this rate of deflection, it sends a jitter signal to SCADA.

provides some indications of the possible location of the problem.

When the jitter is established to be fault related, the diagnosis is made based on the initial information available from the analysis. Generally, there are 6 areas in the power system where there can be a possibility of a fault. These problem areas are:

- Transducers (Voltage and current).
- Analogue input ranging boards in RTUs.
- Other faults in RTU.
- Data Concentrators.
- Power System.
- SCADA.

The diagnosis involves choosing an appropriate fault area from the above areas and suggesting the technician of some possible faults in the problem area.

The knowledge process thus formatted, is then represented in the following three modules:

1. Expert Knowledge.
2. General Knowledge.
3. Common Sense.

### 3.1 Expert Knowledge

Expert knowledge is employed to:

- Analyse the jittery analogs.
- Determine whether the jitter is fault related.
- Suggest possible diagnosis.

The rules designed for the expert knowledge base contains the goal in the "action" part and the elements needed to satisfy the goal in the "conditions" part. Each goal is evaluated in a context which is the expert's functional frame work within which the analysis and diagnosis are carried out. If there exists a condition related to "expert database" ,"general knowledge", or "common sense" among the conditions of a rule, a request is made by the inference engine to the respective data or knowledge base for its evaluation. Some example rules of this situation are shown below:

#### Expert-rule 1:

IF the analogue's circuit has a parallel circuit<sup>3</sup>

<sup>3</sup> The underline section states that the condition has a procedure attached to it.

THEN the analogue history of both the analogues must be compared.

#### Expert-rule 2:

IF two analogues are measured in series

ANDIF there is no power diversion between the two analogues

AND the ?MW<sup>4</sup> of the analogue and the ?MW of the series analogue are same

THEN proceed to diagnosis.

Rule 1 needs establishment of the fact that the jittery analogue's circuit has a parallel circuit using the information from "general knowledge" on how to find a parallel circuit and proceeds further with the analysis. In rule 2, the inference needs the MW (Mega Watts) value from the expert system database to establish that both the values are identical. In general, it is seen that each condition in the rules based on "expert knowledge" has a procedure attached to it, which access the information necessary for their processing from the database and/or the other knowledge bases of the system.

### 3.2 General Knowledge and Common Sense

Domain experts in power system operations tend to use some heuristics which they regard as "general knowledge"<sup>5</sup> or "common sense". Most of these heuristics are commonly used for decision making and problem solving. The "general knowledge" involves basic technical knowledge such as ways to find a parallel circuit, a triangle rule, and ways to compare the analogues. The "common sense" is basically the expert's strategic knowledge used in the diagnosis. A best example for this would be the choice of an appropriate fault area from the most probable range of problem areas.

#### 3.2.1. Modelling General Knowledge

During the analysis of a problem, the expert uses general technical knowledge; for example, as a part of analysis, the human expert searches for a parallel or series line with respect to the line being analysed. The procedure for finding a series or parallel line is same for any circuit or power line. Knowledge like this can be separated from main expert knowledge and set in a separate knowledge base. This separation will reduce the confusion in management of large knowledge

<sup>4</sup> The ? before a word indicates that it is a variable.

<sup>5</sup> The general knowledge in this system is a combination of common sense and technical knowledge.

bases and also improves the portability<sup>6</sup> of the knowledge. Consider rule 1 stated in the previous section. To establish whether the circuit has a parallel circuit, the knowledge should have a procedure to find the parallel circuit. This is where the general knowledge is used. The knowledge of this knowledge base is also represented in IF... THEN rules. The rule structure of the procedure to establish a parallel circuit would be as follows:

**General rule 1:**

IF the analogue's line has a parallel line  
by observation  
AND both the lines are connected to same  
bus  
THEN the analogue's circuit has a parallel circuit.

Note that the "conditions" part in the above rule involves some procedures. These procedures are again "general" in case and are included in the same knowledge base. Consequently, the rules in this knowledge base will not use the rules of any other knowledge base, and once the procedure is passed to this knowledge base, only the output is returned to the rule which called for it. This is also one of the reasons why this knowledge is portable.

### 3.2.1 Modelling Strategic Common Sense

During fault diagnosis, it is always possible that there exists more than one diagnostic solutions for a particular problem. In these situations, the decision is made to select the most opportune diagnosis among the set of possible diagnoses. This decision making is done "unconsciously" by the human expert by analysing possible diagnoses against some predefined and preferred criteria, such as time and cost involved in the repair, as will be discussed later. This type of decision making involves strategic common sense which can be modelled in an expert system using a method proposed by Mussi [1]. The implementation of our system is based on this method.

In this implementation, each diagnosis will have a certain belief level with respect to the presence of the fault (eg. probable, most\_probable, certain). This belief level states the extent of the surety in the diagnostic result. Let us take the following diagnostic rule from the expert knowledge base:

**initial\_diag\_rule 1:**

IF only one ?analogue of ?RTU is jittery

---

<sup>6</sup> The general knowledge used in this system can be used in other power system fault diagnostic systems.

THEN the presence of problem in ?word or ?card of ?RTU is most\_probable.

This rule states the fact that if only one analogue of the RTU is jittery, then it is most probable that the "word address" or the "card address" of that RTU is faulty. At this stage, the information available from the analysis, may also point to possible problems in SCADA (wrong inputs, communication problems, etc.) , and it cannot be ruled out. To make the presence of the problem more "certain", further investigation has to be done by applying a special set of rules which always provide an output depending on the truth value of its conditions. These outputs are in the form of evidences with a certainty factor associated with it, and are used in the final diagnostic rules which are the final decision making rules having the "evidences" in their "conditions" part and the related diagnosis in the "conclusions" part of the rule. If all the "conditions" of a rule are true, then the diagnosis will have the highest value of the belief level. This diagnosis (with highest "value") is chosen as the final diagnostic solution. Contradiction arises when more\_than\_one rule satisfies all their "conditions" (multiple final solutions) or no rule satisfies all the "conditions" (no final solution). It is here that the "strategic common sense" is used to resolve the contradictions.

### 3.2.3. Strategic Common Sense

Apart from the belief level, each diagnosis has some "preference parameters" (such as evidences, time\_consuming, and cost) attached to it. Each preference parameter has a range of values: low, medium, high. The selection of diagnosis using "strategic common sense" is based on the "preference criteria" which refer to preference parameters and their values. For example, if P is the preference parameter "evidence", a likely "preference criterion" would be: *"select the diagnosis with highest evidences"*. There are similar preference criteria for the other preference parameters.

A diagnostic solution which satisfies all the "preference criteria" is chosen as the most opportune solution. However, it is hard to find a diagnostic solution which has

**evidence** >>> high;

**time\_consuming** >>> low;

**cost** >>> low;

and so on.

Some diagnoses might have "high" evidence but it may be "more" time consuming to solve or involve "high" cost. Hence there is a need to select the diagnosis by prioritising the preference criteria. This priority has to be defined by human experts. During human expert's diagnosis, this prioritisation is

"unconsciously" done. This "unconscious" knowledge can be derived from human expert's rules by analysing them [1]. This meta-knowledge base is represented as meta-rules which act as strategic common sense. An example of a meta-rule is given below for two preference criteria X and Y, where X = "Select the diagnosis with lowest value of time\_consuming", and Y = "Select the diagnosis with lowest cost value", and X is more important than Y by domain expert's initial priority assignment:

**common\_sense\_r\_1:**

```

    IF      Fin_diag_1_pparvX      =
    Fin_diag_2_pparvX

    THEN choose between Fin_diag_1 and
    Fin_diag_2                      with respect to Y
    
```

where, Fin\_diag\_1 and Fin\_diag\_2 are the two conflicting diagnostic rules and pparvX is the value of the preference parameter of the preference criterion X.

The above rule states that, if the preference parameter value of the preference criterion X (eg. time\_consuming of diagnosis\_1 >>> medium) of one of the conflicting diagnosis is equal to that of the other diagnosis (eg. time\_consuming of diagnosis\_2 >>> medium) then select the best diagnosis with respect to the value of preference criterion Y (ie. cost).

The above mentioned rule is the simplest meta-rule in this knowledge base and is applicable if there are only two conflicting diagnoses and each one of those has only two preference criteria attached to them. More Complex rules are used when there are more than two diagnostic rules in conflict and each rule has more than two preference criteria. A more detailed description of these rules and strategic common sense can be found in Rayudu [3].

### 4 Database

To integrate an on-line system with SCADA, the real time network data has to be stored in a database in such a way that they can be accessed by the system. Such databases are already in use by other application programs in the control centre but they do not provide all the information our system needs. Hence we had to develop our own database. The database stores the information in five different levels developed in object oriented representational approach (Figure 3). Level 1 & 2 stores the information of jitter analogues and their values. Level 3 consists of the RTU information and the analogues attached to each of them. The data concentrator information are stored in level 4 along with RTUs addresses connected to it and the final level consists of the power system network data necessary for the expert system. In many situations,

the access of the database is fairly hierarchical with respect to the stages of the analysis and diagnosis.

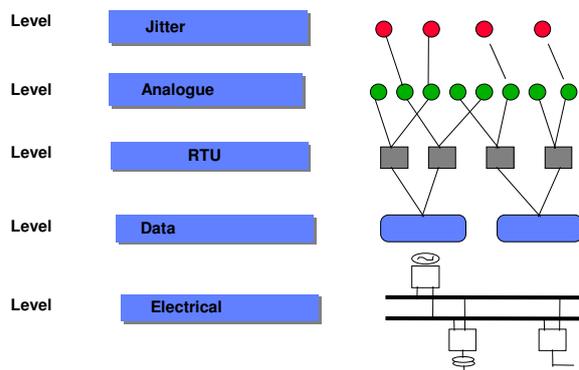


Figure 3: Network database layers.

### 5 Diagnostic Algorithm

A diagnostic algorithm with compound structure of sub-inference engines is constructed based on the analysis of the jitter analog problems by the domain expert. The general structure of the algorithm is shown in Figure 4. The entire algorithm includes task-specific sub-inference engines in two-levels. The first level, Analysis Inference Engine (AIE), is responsible for analysing each and every analog in the analog list. This inference engine takes the analog list and analyses the analogs using the expert and general knowledge bases. The second level inference engine, Diagnostic Inference Engine (DIE), is responsible for diagnosing the analogs by using the information provided by AIE. This inference engine not only uses the expert and general knowledge bases in its decision process but also has the access to the common sense knowledge using it when ever it is needed.

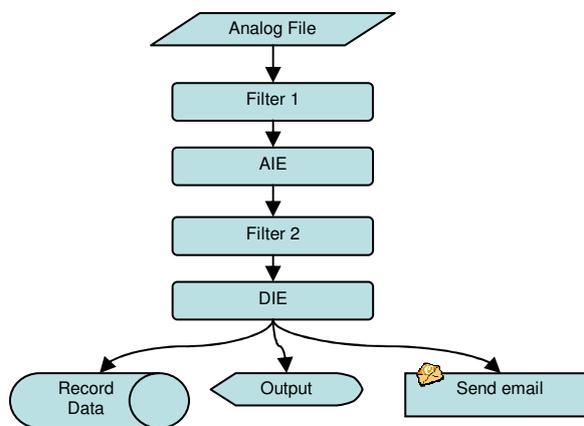


Figure 4: Compound structure of Diagnostic algorithm.

AJA's diagnostic algorithm uses a combination of backward chaining and forward chaining mechanisms and some logical control structures from traditional programming. Backward chaining is used in the AIE, and forward chaining is used in the DIE. Control structures are used in firing of "final fault diagnosis" rules where all the rules succeed whether or not all the "conditions" of each rule are "true". The control structures of standard programming are also used in searching.

The interface between AJA and SCADA is done in separate modules. This interface transports the necessary real-time information from SCADA to the "expert database" where it is stored in the levels specified.

The entire system is designed to operate "automatically" without any user interaction and only at a specific time it shuts itself off after the diagnosis. The output of the operation is presented in the form of a list of "reports" usually sent as a mail message to the concerned technical personnel. Other facilities, such as keeping track of the most jittery analogues and keeping a record of problem frequency of each analogue are also provided. In addition, a database of diagnosed analogs is maintained.

## 6 A Case Study

A part of jitter analog data obtained from SCADA is shown in the Figure 5.

ANALOG	Jitter Count
WPRASYMW	543
WPRASYMV	213
SBKKAI1MW	32
TMKT2TAB	108
UTKCOB2A	89
ROXT1MV	57
AVIBEN1A	446
AVIBEN1MV	0
BRYASBA	86
INVGORA	146
INVGORMW	103
INVGORV	132
WPRASYA	36
TMKT4MW	0
BENG6MW	12

Figure 5: Sample Analog list with jitter counts.

This file is then fed to AJA. The preliminary filter (FILTER 1 in Fig. 4) filters all the unnecessary analogs and the resulting analog list is sent for

analysis. The analysis of these analogs is carried by Analysis Inference Engine (AIE) where every analog is analysed. After the analysis is complete, a second filter is used to filter the analogs based on the output from AIE. The output list from FILTER 2 is then used for diagnosis of the analogs. The diagnosis is done by Diagnosis Inference Engine (DIE) where the output result is then written onto two files: one for sending to the technician and another for records. An Example of the diagnostic output of AJA is shown in the Figure 6.

ANALOG	RTU	CA	WA
--------	-----	----	----

WPRASYMW	2	3	6
WPRASYMV	2	3	4

Diagnosis: Faulty card 104.

AVIBEN1A	6	2	8
----------	---	---	---

Diagnosis: Check Card 107.

INVGORA	5	3	3
INVGORMW	5	4	2
INVGORV	5	4	5

Diagnosis: Check the transducers.

Figure 6: Sample of diagnosed output from AJA.

The experience with a number of AJA has shown that:

In general, problem analogs are identified and are sent to the technician without any user interaction, thereby saving the expert's time.

The procedure presented here provides satisfactory results for larger extent of the jitter problems. Over seventy percent of the analysed analog list were found to be accurate (i.e. analogs which jitter due to faults). This is closer to our expectations as the knowledge encoded in AJA, according to the domain expert, is capable of solving only 84% of the jitter analog problems.

## 7 Conclusion

A power system fault analysis and diagnosis expert system is developed with the incorporation of "general knowledge" and "strategic common sense" as decision tools in conjunction with the domain expert's problem solving knowledge. The system is an "automatic" on-line operating software which runs once a day to analyse and diagnose the "jitter" analogues of the power system. The expert knowledge base consists of the domain expert's heuristic rules which basically drive the inferences of the system. The general knowledge base consists of rules relating to the general technical knowledge of

the power systems. It is used where there is a need for general observations in the problem solving process. The common sense knowledge base consists of meta-rules relating to the unconscious decision making strategy of the domain expert. This knowledge is used in the problem diagnosis part where the conflicting diagnostic rules are resolved for the most promising diagnostic solution.

The separation of expert and general knowledge improves the maintainability of the knowledge bases and also helps in knowledge portability and reuse. Research is underway to investigate the portability and reuse of these knowledge bases as the expert system will be applied to the other control centres in the future.

## 8 Acknowledgements

This project is funded by Transpower NZ Ltd. Some part of this research has been performed at Transpower Lab, C-fACS, Lincoln University

## 9 References

- [1] Mussi, S., "A Method for putting strategic common sense into expert systems", *IEEE Trans. on Knowledge and Data Engineering*, Vol. 5, No. 3, pp. 369-385 (June 1993).
- [2] Adibi, M. M., Kafka, R. J., Clements, K. A., and Stovall, J. P., "Integration of Remote Measurement Calibration with State Estimation - A Feasibility Study", *IEEE Trans. on Power Systems*, vol. 7, no. 3, pp. 1164-1172, (Aug. 1992).
- [3] Rayudu, R. K., Papps, K., Silke, T., and Samarasinghe, S., "Towards the design of an Intelligent System in Power Systems", *Proc. 10th CEPST*, Christchurch, New Zealand, September, pp. 310-320, (1994).
- [4] Sugihara, H., "A Practical Expert System for indicating faulty sections within a control centre", *ESAP' 1993*, Australia, pp. 236-241, (1993).
- [5] Pfau-Wagenbauer, M., and Nejd, W., "Integrating Model based and Heuristic Features in a Real-Time Expert System", *IEEE Expert*, , pp. 12-18 (August, 1993).
- [6] Adibi, M. M. and Kafka, R. J., "Minimization of Uncertainties in Analog Measurements for use in State Estimation", *IEEE Trans. on Power Systems*, vol. 5, no. 3, pp. 902-910, (Aug. 1990).
- [7] Zen, F.P., Gunara, B.E., Hidayat, W., Thalib, Z.A., Zainuddin, H., Aminuddin, J., "Application of Artificial Neural Network in Jitter Analysis of Dispersion-Managed Communication System", *ArXiv e-print archive*, Cornell University, (July 2004).
- [8] Norman Sadeh-Konieczpol and David Hildum and Dag Kjenstad and Allen Tseng and Thomas J. Laliberty and John McA'Nulty, "A Blackboard Architecture for Integrating Process Planning and Production Scheduling", *Concurrent Engineering: Research & Applications*, Vol 6, No 2, (June 1998).
- [9] Hayes-Roth, B. 1985. A blackboard architecture for control. *Artif. Intell.* 26, 3, 251-321, (Aug. 1985).
- [10] Ebrahimian R., and Baldick R., "State Estimation Distributed Processing", *IEEE Trans. on Power Systems*, vol. 15, no. 4, pp. 1240-1246, Nov. (2000).
- [11] C.-C. Yang and Y.-Y. Hsu, "Estimation of line flows and bus voltages using decision trees," *IEEE Trans. on Power Systems*, vol. 9, no. 3, pp.1569-1574, Aug. (1994).
- [12] Jakovljevic, S.; Kezunovic, M., "Software for enhanced monitoring in integrated substations", *Power Tech Conference Proceedings, 2003 IEEE Bologna*, Volume 4, 23-26 June 2003 Page(s):6 pp. Vol.4 (2003)